

In the Drawings:

Please replace Figure 3 with the enclosed new Figure 3 showing the uplink processor 39A and the downlink processor 39B of the MLC 39 as described at page 9, line 9 and page 9, line 26.

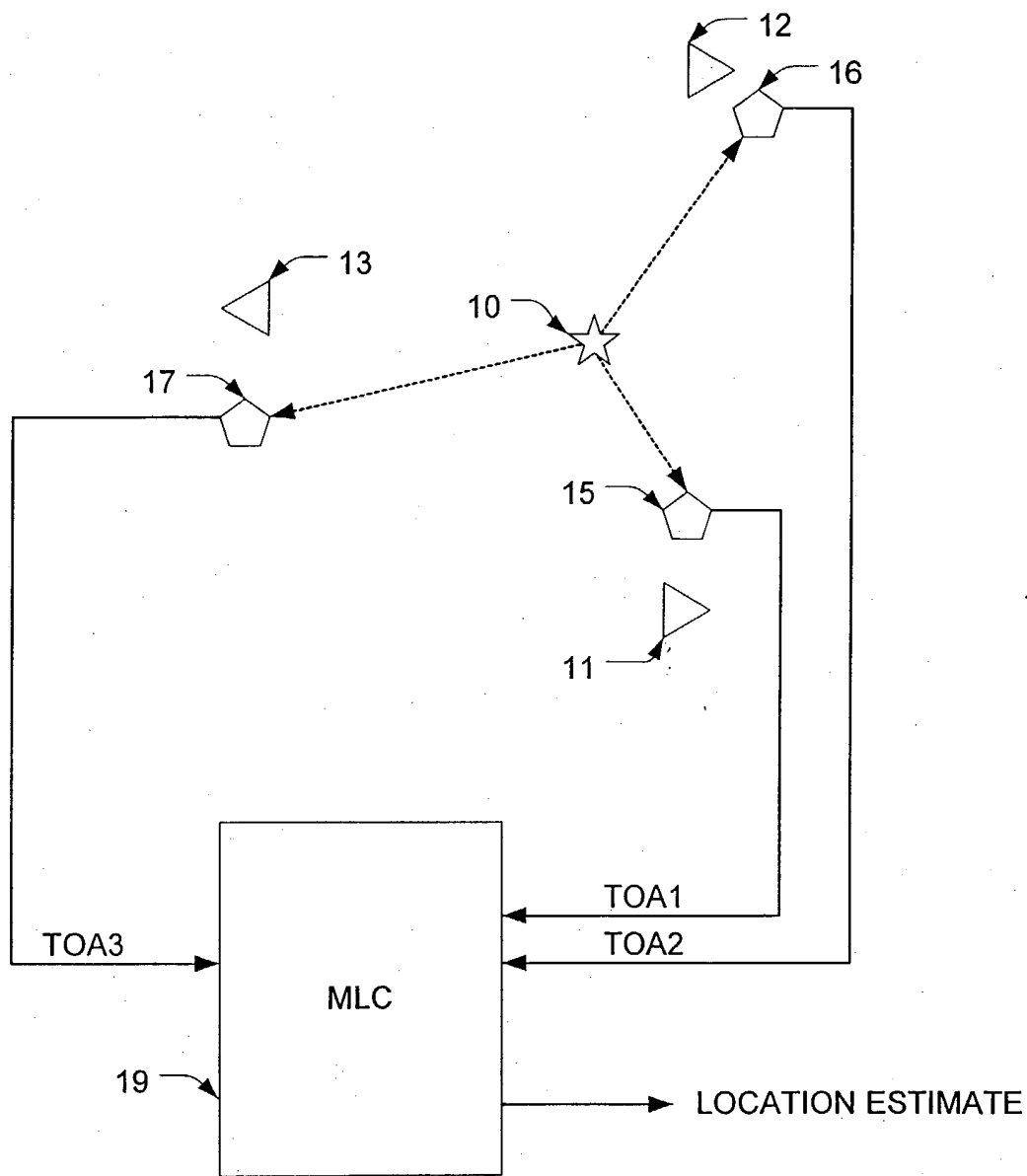
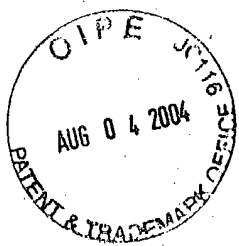


FIG. 1
PRIOR ART

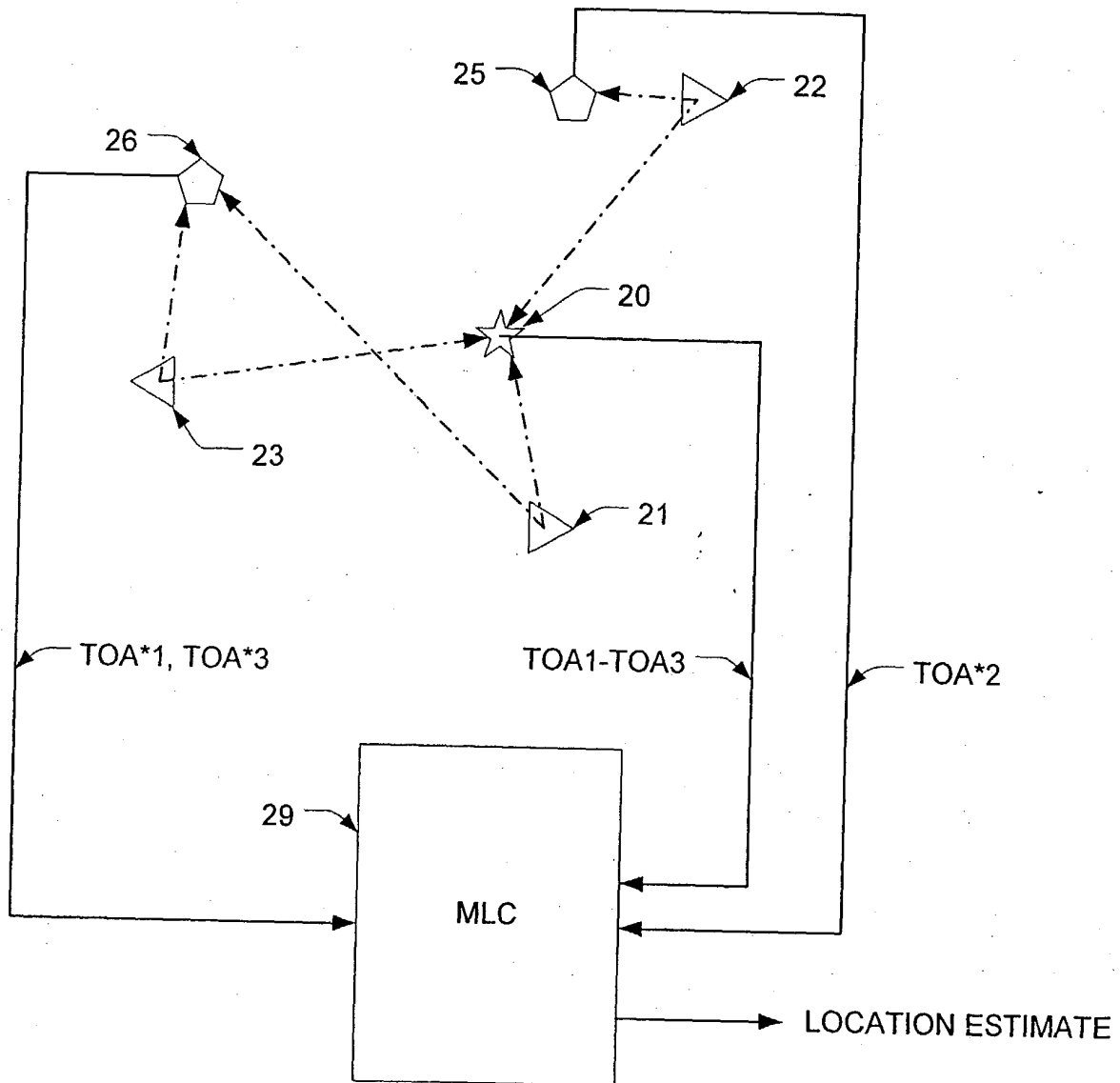


FIG. 2
PRIOR ART



REPLACEMENT SHEET

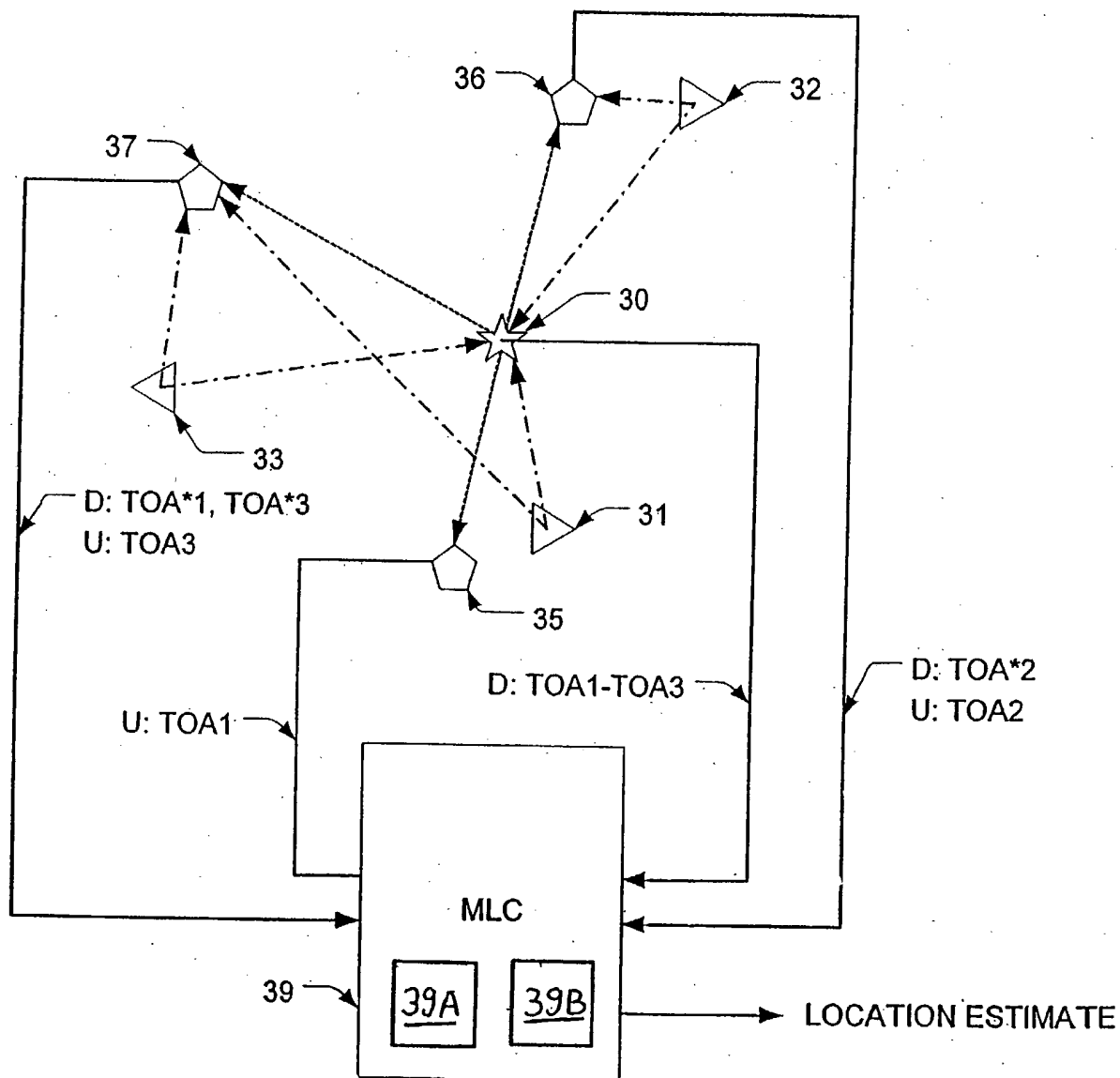


FIG. 3

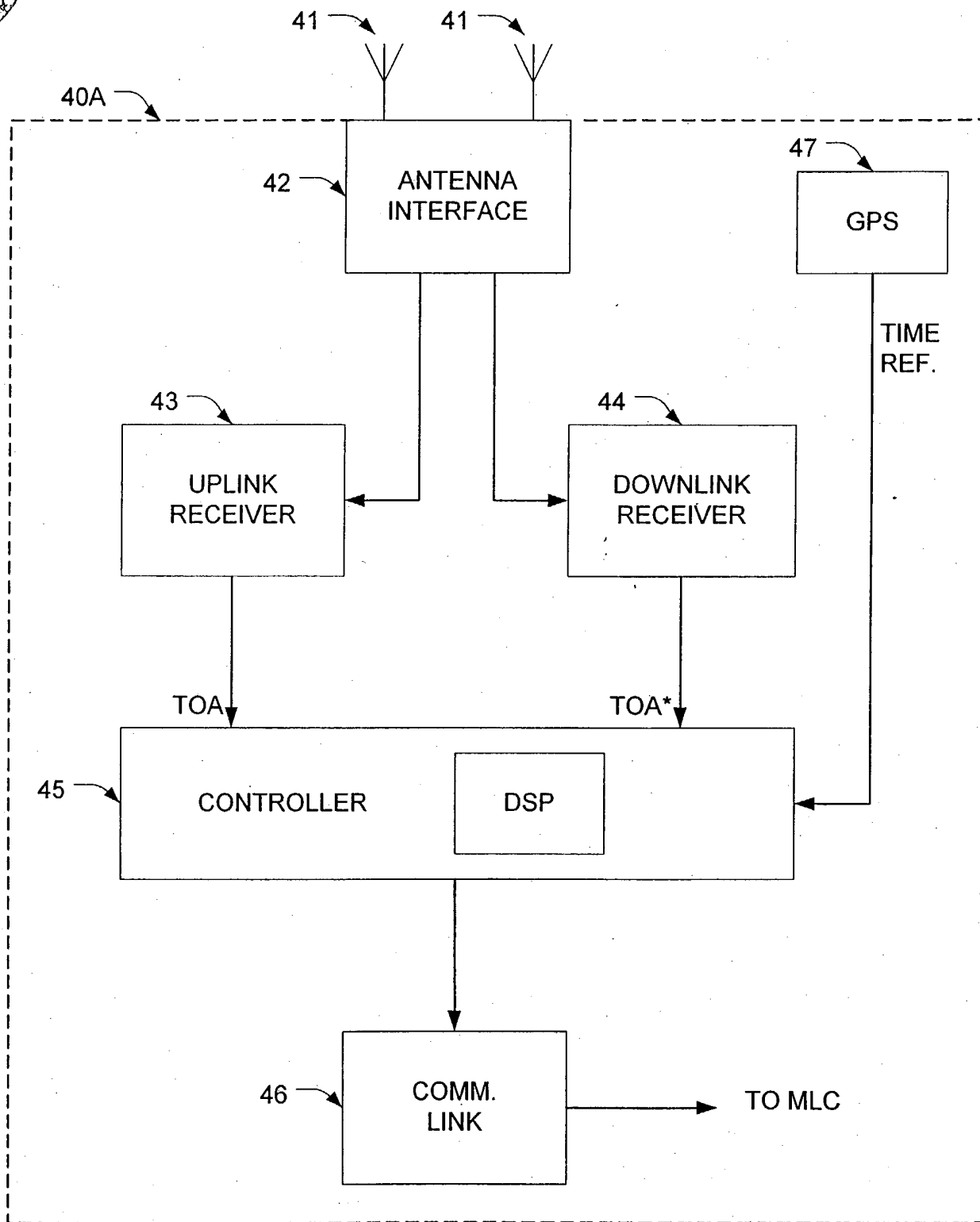


FIG. 4A

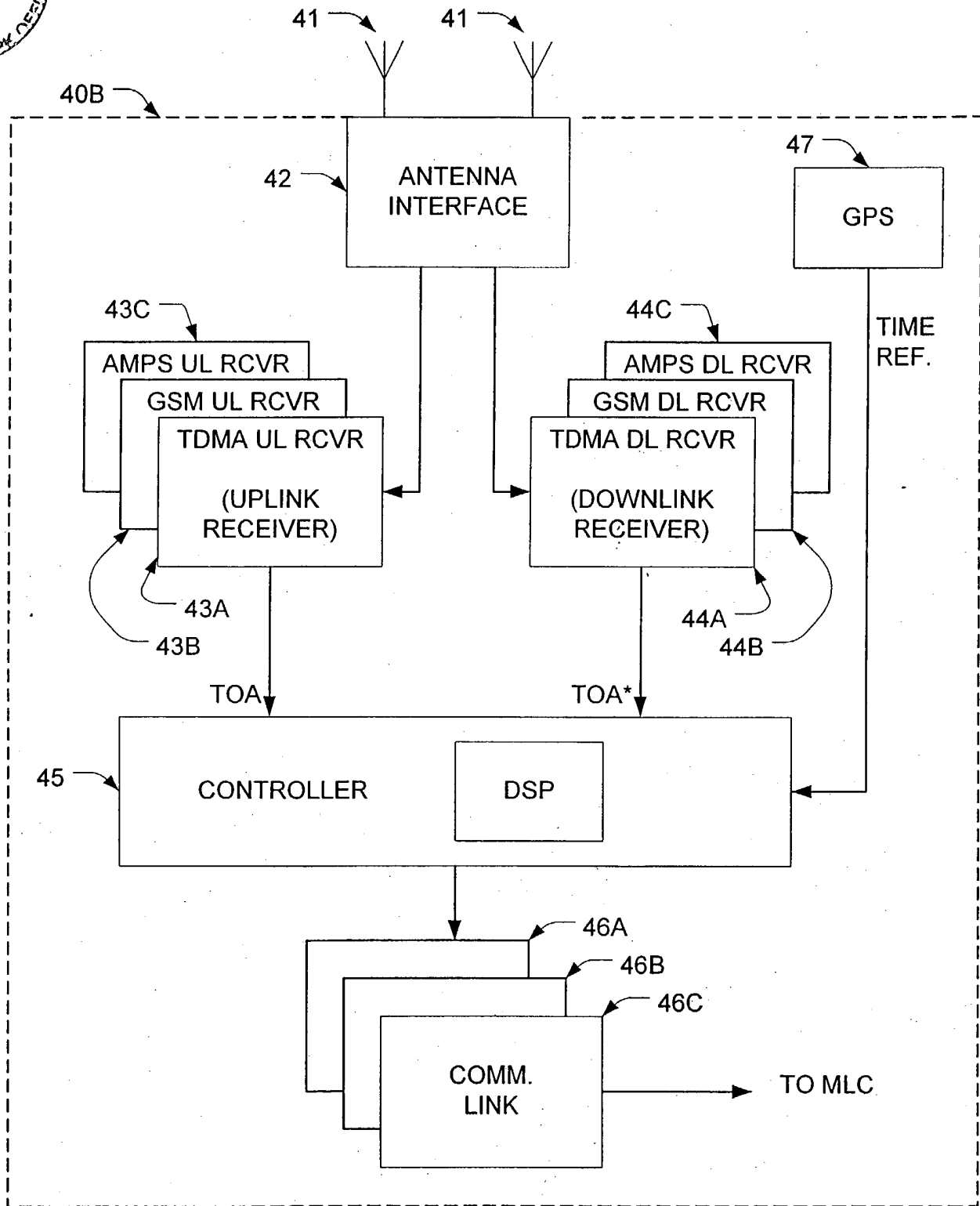
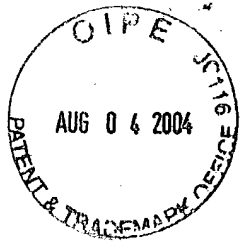


FIG. 4B

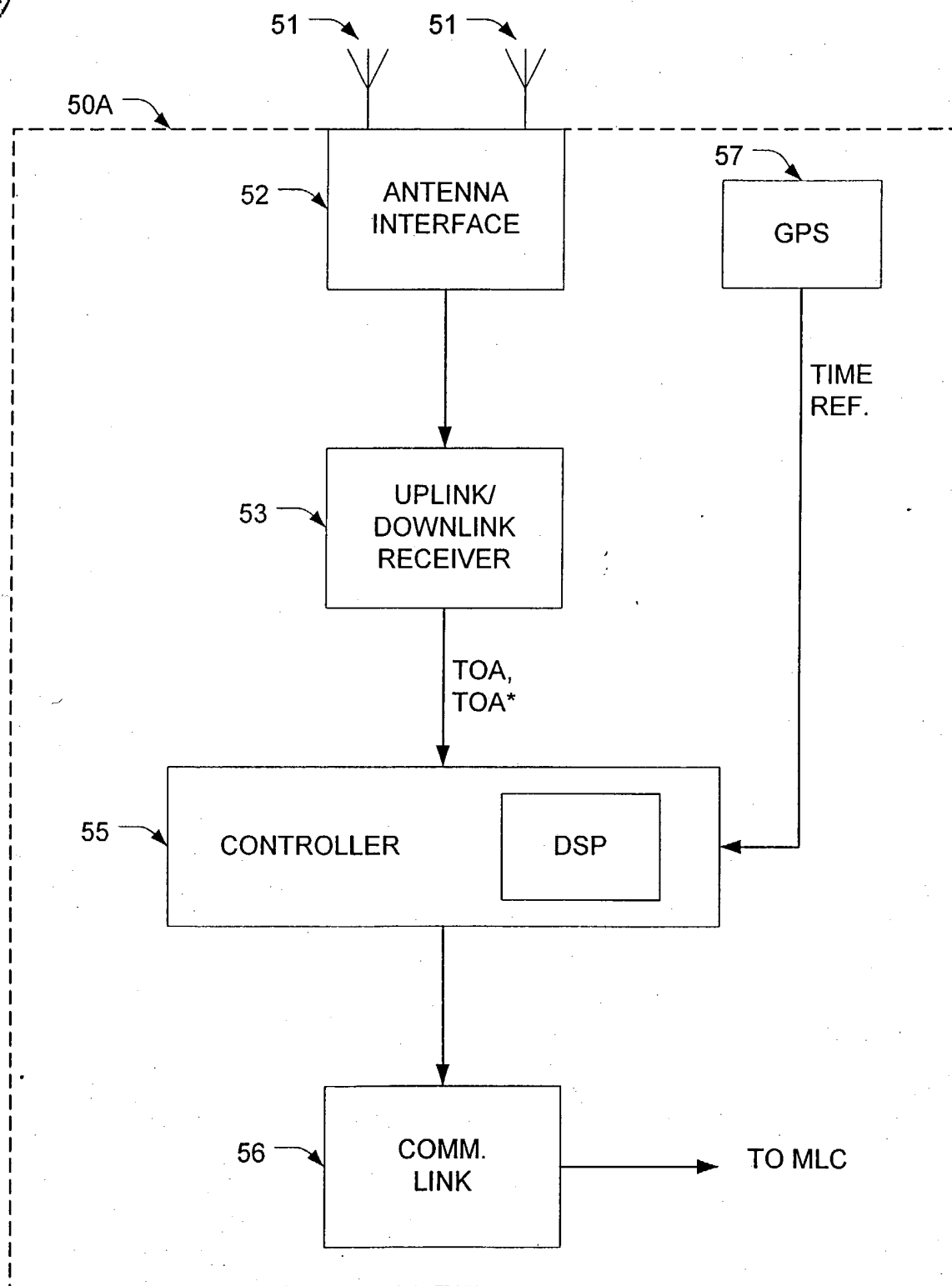


FIG. 5A

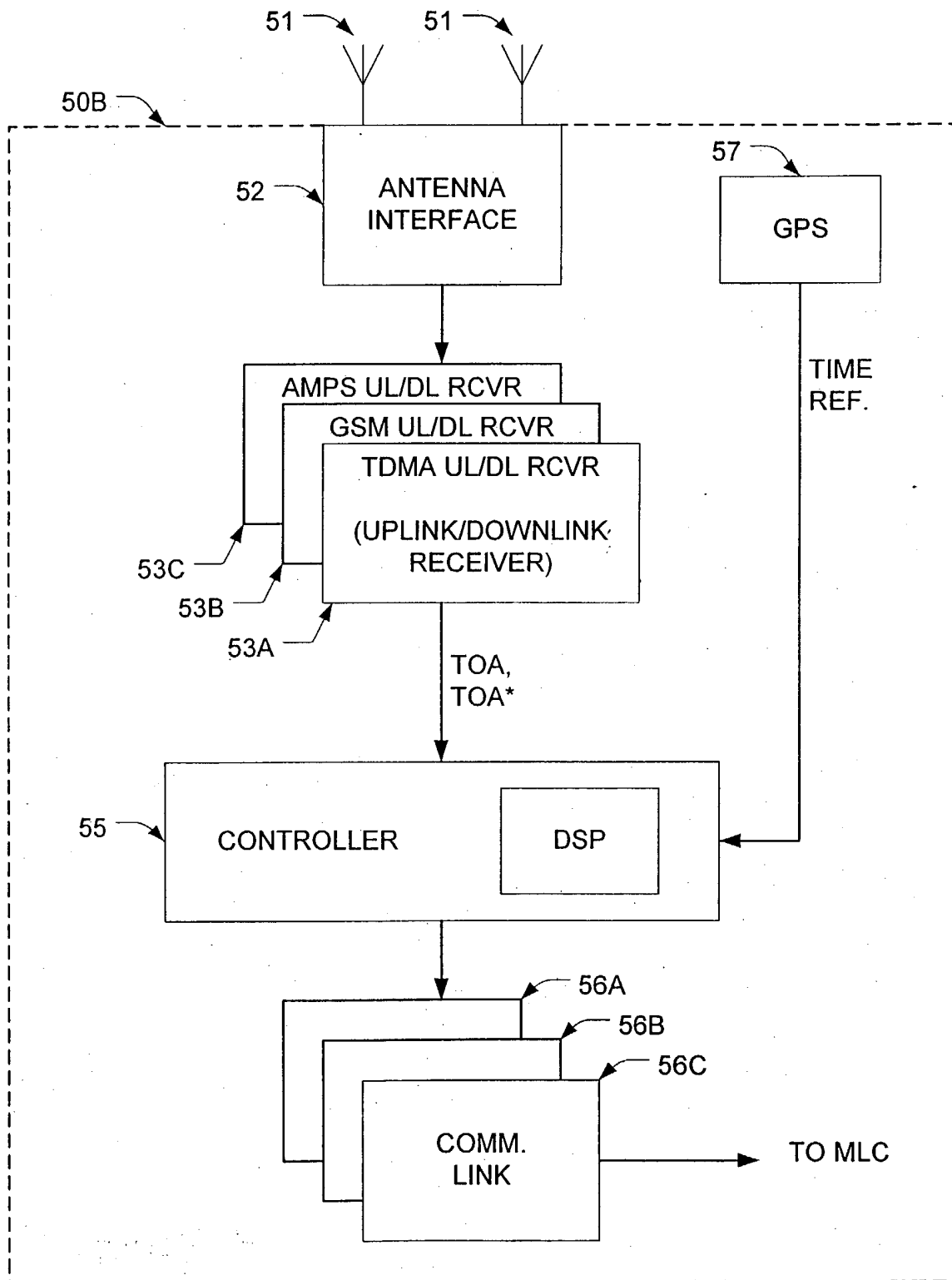
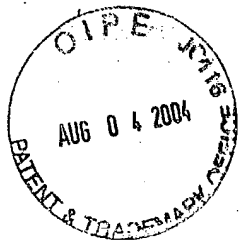


FIG. 5B

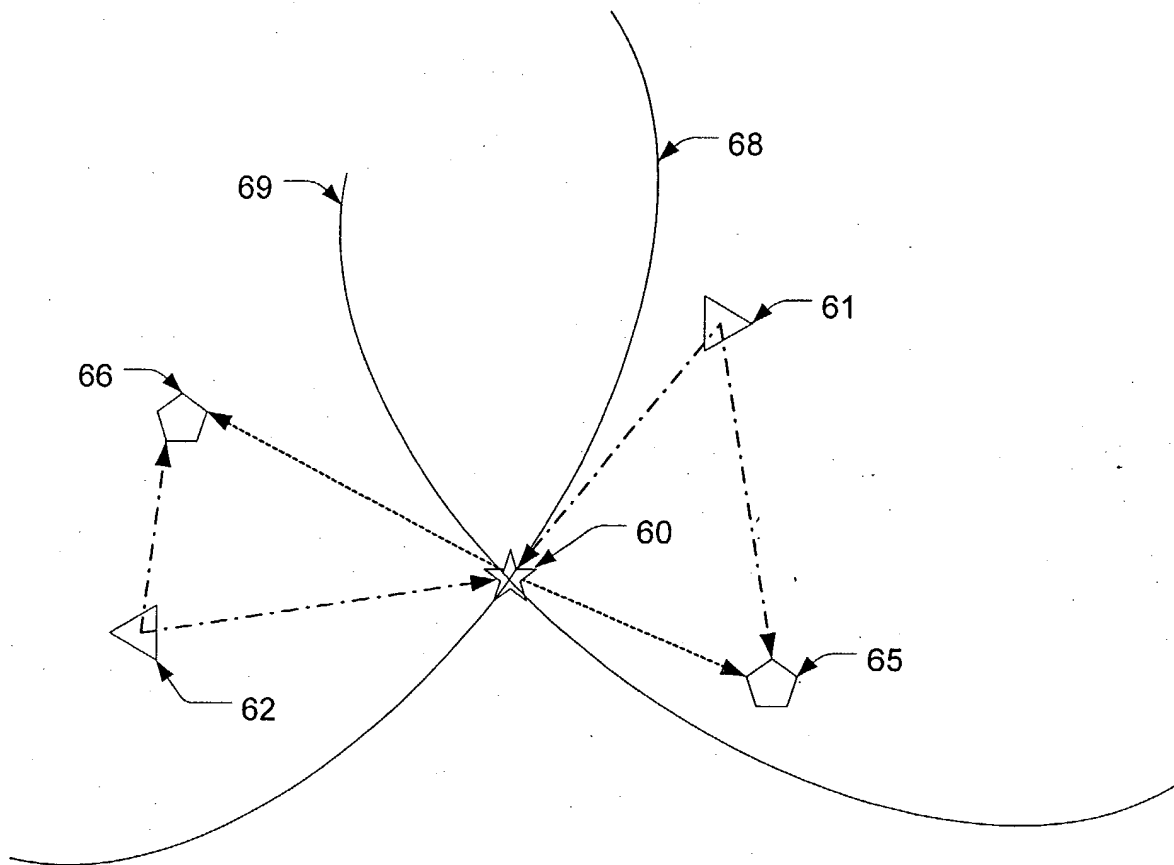


FIG. 6